

# PATENT ABSTRACTS OF JAPAN

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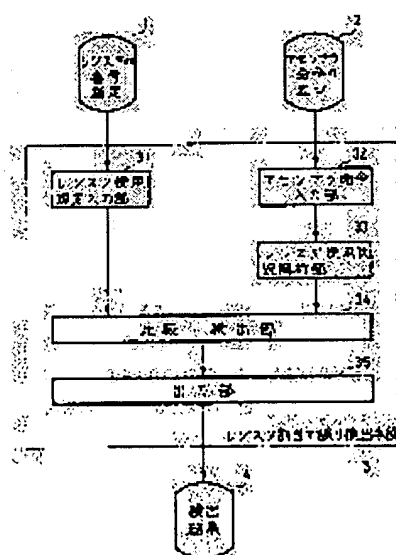
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## (54) SYSTEM FOR DETECTING ERROR IN REGISTER ALLOCATION

### (57)Abstract:

**PURPOSE:** To attain the quick and accurate mechanical detection of errors allocated to register by using a means which compares the using rules of a register with the analysis result of the using condition of the register in an array of assembler instructions and detects the allocation errors of the register in said array of assembler instructions.

**CONSTITUTION:** A using condition analyzing part 33 analyzes the operand of an assembler instruction read by an assembler instruction input part 32 to recognize the using condition of a register. A comparison/detection part 34 compares the number designation 1 of the register read through a register using rule input part 31 with the using condition of the register recognized by the part 33. Then it is checked whether such a register that hold not be used in an array 2 of assembler instructions is used or not or whether a register to be used is used or not. Then an error allocated to the register is detected in said array 2.



## LEGAL STATUS

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